

Notice of References Cited	Application/Cont. No. 09/598,713	Applicant(s)/Patent Under Reexamination DOUGLAS, JONATHAN P.	
	Examiner David J. Huisman	Art Unit 2183	Page 1 of 1

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*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
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	B	US-6,170,998	01-2001	Yamamoto et al.	717/154
	C	US-			
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	M	US-			

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*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
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NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	IBM Technical Disclosure Bulletin NN9204269, "Return Address Stack Cache," April 1992, pp. 269-271
	V	Hennessy & Patterson, "Computer Architecture - A Quantitative Approach, 2nd Edition," 1996, pg.141.
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
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